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April 1<sup>st</sup>, 2010 Renesas Electronics Corporation

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HAT1026R

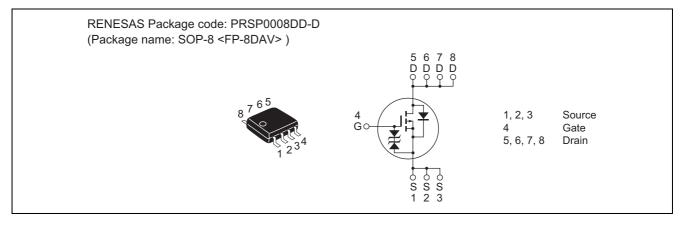
Silicon P Channel Power MOS FET High Speed Power Switching

> REJ03G1148-1000 (Previous: ADE-208-457H) Rev.10.00 Sep 07, 2005

## Features

- Low on-resistance
- Capable of 4 V gate drive
- Low drive current
- High density mounting

### Outline





# **Absolute Maximum Ratings**

			$(Ta = 25^{\circ}C)$
Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	-30	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	ID	-7	A
Drain peak current	I <sub>D (pulse)</sub> Note 1	-56	A
Body-drain diode reverse drain current	I <sub>DR</sub>	-7	А
Channel dissipation	Pch Note 2	2.5	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

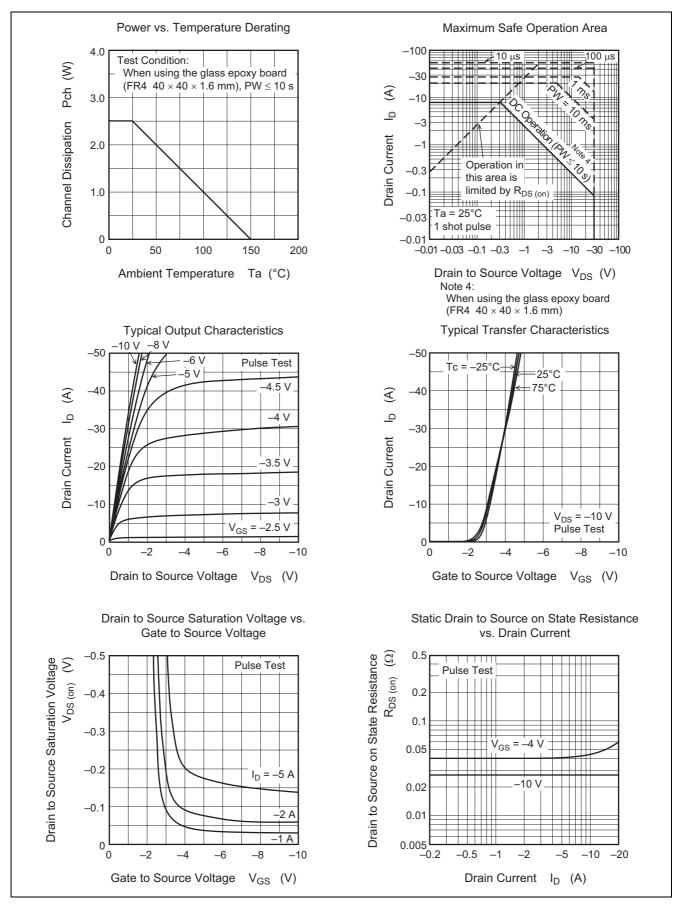
2. When using the glass epoxy board (FR4 40  $\times$  40  $\times$  1.6 mm), PW  $\leq$  10 s

# **Electrical Characteristics**

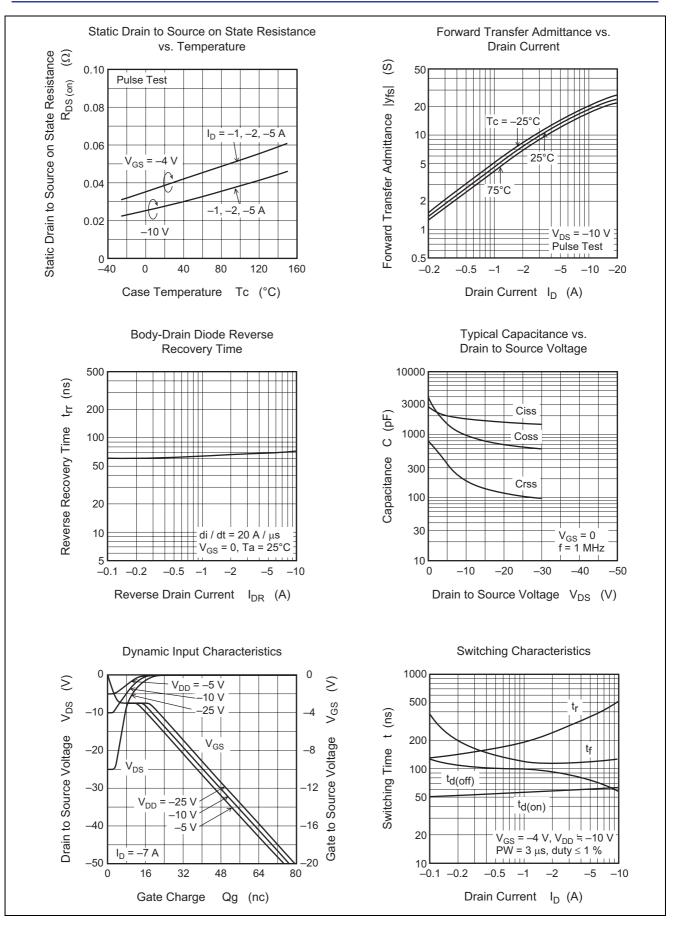
						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	-30		—	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V (BR) GSS	±20	_	—	V	$I_{G} = \pm 100 \ \mu A, V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	—		±10	μA	$V_{GS} = \pm 16 \text{ V}, \text{ V}_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	—		-10	μA	$V_{DS} = -30 V, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	-1.0		-2.5	V	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	—	0.028	0.037	Ω	$I_D = -4 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note 3}}$
	R <sub>DS (on)</sub>		0.04	0.065	Ω	$I_D = -4 \text{ A}, V_{GS} = -4 \text{ V}^{\text{Note 3}}$
Forward transfer admittance	y <sub>fs</sub>	8	12		S	$I_D = -4 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note 3}}$
Input capacitance	Ciss		1700		pF	V <sub>DS</sub> = -10 V
Output capacitance	Coss		1000		pF	V <sub>GS</sub> = 0
Reverse transfer capacitance	Crss		190		pF	f = 1 MHz
Turn-on delay time	t <sub>d (on)</sub>		60		ns	$V_{GS} = -4 V, I_D = -4 A,$
Rise time	tr		330		ns	V <sub>DD</sub> ≅ −10 V
Turn-off delay time	t <sub>d (off)</sub>		80		ns	
Fall time	t <sub>f</sub>		120		ns	
Body-drain diode forward voltage	V <sub>DF</sub>		-0.9	-1.4	V	$I_{\rm F} = -7$ A, $V_{\rm GS} = 0^{\rm Note 3}$
Body-drain diode reverse recovery time	t <sub>rr</sub>		70	_	ns	$I_{\rm F} = -7$ A, $V_{\rm GS} = 0$
						di <sub>F</sub> /dt = 20 A/µs

Note: 3. Pulse test

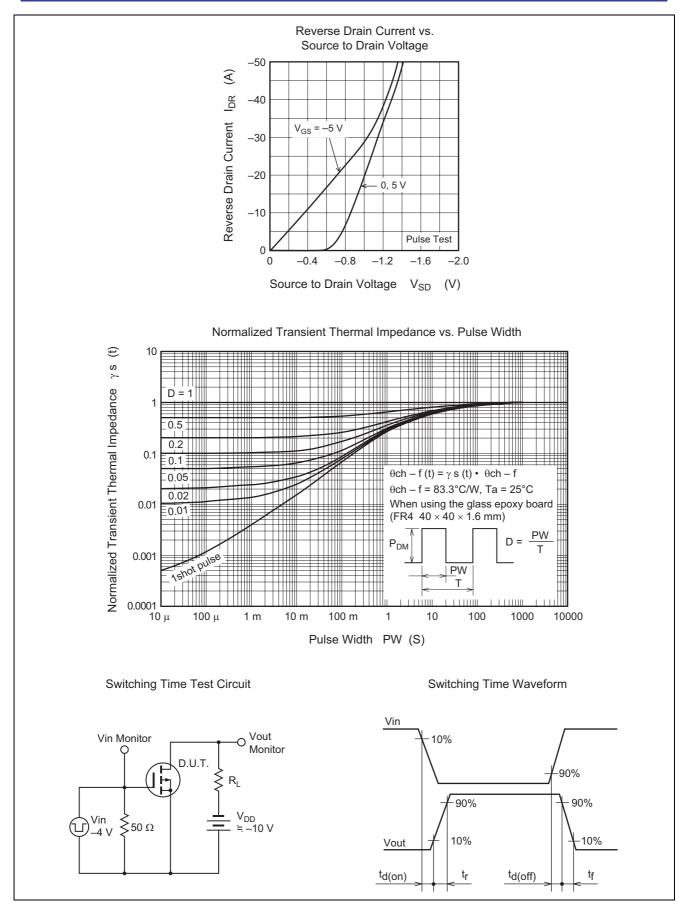
### **Main Characteristics**





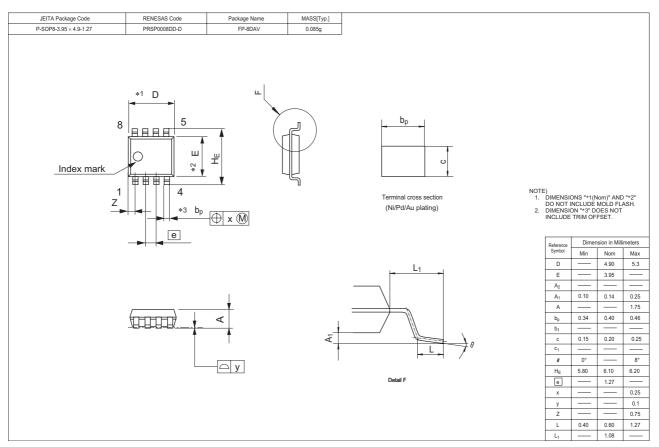








## **Package Dimensions**



# **Ordering Information**

Part Name	Quantity	Shipping Container
HAT1026R-EL-E	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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